

WHAT IS CLAIMED IS:

1. A liquid crystal display, comprising:

a substrate;

5 a first signal line formed on the substrate and extending in a first direction;

a second signal line intersecting the first direction;

a first pixel electrode formed in a pixel area defined by intersections of the first signal line and the second signal line, said first

10 pixel electrode formed substantially parallel to the first signal line;

a pixel signal line connected to the pixel electrode;

a switching element connected to the first signal line, the second signal line, and the pixel signal line;

15 a first common electrode formed in the pixel area parallel to said first pixel electrode;

a common signal line formed in the pixel area connected to said common electrode;

a first capacitor electrode formed in the pixel area connected to the pixel signal line;

20 a second capacitor electrode formed in the pixel area connected to said common signal line;

a second pixel electrode formed in the pixel area opposite to the first pixel electrode with respect to said capacitor electrode and connected to the pixel signal line; and

a second common electrode formed in the pixel area, said
5 second common electrode opposite to the first common electrode with respect to said capacitor electrode and connected to the common signal line.

2. The liquid crystal display of claim 1, wherein the pixel signal
10 line overlaps the common signal line.

3. The liquid crystal display of claim 1, wherein the common signal line is parallel to the second signal line.

4. The liquid crystal display of claim 1 and 3, wherein the
15 distance between the common signal line and the second signal line is shorter than the distance between the pixel signal line and the switching element.

5. The liquid crystal display of claim 1, wherein the first signal
20 line bends at a positive or negative angle with respect to the perpendicular direction of the second signal line.

6. The liquid crystal display of claim 1, wherein the first signal line is bent at a positive or negative angle with respect to the direction of a rubbing direction on the substrate.

5 7. The liquid crystal display of claim 1, wherein the first capacitor electrode is triangular in shape.

8. The liquid crystal display of claim 1, wherein the second capacitor electrode is triangular in shape.

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9. The liquid crystal display of claim 1, wherein the first common electrode is disposed nearer to the first signal line than the first pixel electrode.

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10. The liquid crystal display of claim 1, wherein the second common electrode is disposed nearer to the first signal line than the second pixel electrode.

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11. The liquid crystal display of claim 1 further comprises a plurality of pixel areas disposed along the direction of the first signal line.

12. The liquid crystal display of claim 1 further comprises a plurality of pixel areas disposed symmetrically with respect to the second signal line therebetween.

5 13. The liquid crystal display of claim 1, 11 and 12, wherein the pixel area is triangular in shape.

14. The liquid crystal display of claim 1, wherein the pixel electrode and the common electrode are disposed on the same planar
10 plane.

15 15. The liquid crystal display of claim 1 and 14, wherein the pixel electrode and the common electrode have a thickness of less than about 2000 Å.

16. The liquid crystal display of claim 1, wherein the capacitor electrodes are disposed in a longitudinal center of the pixel area.

20 17. The liquid crystal display of claim 1, wherein the first capacitor electrode is a part of the first pixel electrode.

18. The liquid crystal display of claim 1, wherein the pixel area has a rectangular shape.

19. The liquid crystal display of claim 1, wherein the first signal
5 is formed of at least one material selected from a group of Al, Al-alloy, Ag, Ag-alloy and its alloy.

20. The liquid crystal display of claim 19, wherein the first signal further comprises a pad layer.

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21. Liquid crystal display, comprising:

a first signal line extending in a fast direction;

a second signal line disposed parallel to the first signal line;

a third signal line disposed at a fast angle with respect to a

15 perpendicular direction of the first signal line, said third signal line

intersecting the first signal line;

a fourth signal line disposed at second angle with respect to

a perpendicular direction of the first signal line, said fourth signal line

intersecting the first signal line;

20 a pixel area having a first part, a second part and a third

part, said pixel area defined by the first, second, third and fourth signal

lines;

a first common electrode extending parallel to the third signal line, said first common electrode disposed in the first part of the pixel;

5 a first pixel electrode extending parallel to the third signal line, said first pixel electrode disposed in the first part of the pixel area;

a second common electrode having a first line and a second line, said first line parallel to the third signal line, said second line parallel to the fourth signal line and said second common electrode disposed in the second part of the pixel area;

10 a second pixel electrode having a third line and a fourth line, said the third line parallel to the third signal line, said fourth line parallel to the fourth signal line and said second pixel electrode disposed in said the second part of the pixel area;

15 a first capacitor electrode electrically connected to the common electrode, said first capacitor electrode disposed in said the second part of the pixel area;

a second capacitor electrode electrically connected to the pixel electrode, said second capacitor electrode disposed in said second part of the pixel area;

20 a third common electrode extending parallel to the fourth signal line, said third common electrode disposed in said third part of the pixel area; and

a third pixel electrode extending parallel to the fourth signal line, said third pixel electrode disposed in the third part of the pixel area.

22. The liquid crystal display of claim 21 further comprises a
5 switching element electrically connected the pixel electrode.

23. The liquid crystal display of claim 21 further comprises a pixel signal line electrically connected to the pixel electrode.

10 24. The liquid crystal display of claim 23 further comprises a common signal line electrically connected to the common electrode.

25. The liquid crystal display of claim 24, wherein the common signal line is substantially parallel to the first signal line.

15 26. The liquid crystal display of claim 25, wherein the pixel signal line overlaps the common signal line.

20 27. The liquid crystal display of claim 26, wherein the common signal line is disposed closer to the first signal line than the pixel signal line.

28. The liquid crystal display of claim 21, wherein the pixel area has a trapezoidal shape.

29. The liquid crystal display of claim 21, wherein the capacitor electrode has a triangular shape.

30. The liquid crystal display of claim 22, wherein the common electrode adjacent to the switching element is disposed nearer to the third or fourth signal line than the pixel electrode.

31. The liquid crystal display of claim 21, wherein the pixel area is symmetrical with respect to the second capacitor electrode.

32. The liquid crystal display of claim 21, where the pixel area is symmetrical to the first signal line therebetween.

33. The liquid crystal display of claim 21, wherein the pixel area is repeated in a row in the perpendicular direction of the first signal line.

34. The liquid crystal display of claim 21, wherein the pixel electrode and the common electrode are disposed on the same plane.

35. The liquid crystal display of claim 21, wherein the pixel electrode and the common electrode have a thickness of less than about 2000Å.

5 36. The liquid crystal display of claim 21, wherein the third or fourth signal line is formed of at least one material selected from a group of Al, Al-alloy, Ag, Ag-alloy and its alloy.

10 37. The liquid crystal display of claim 36, wherein the third or fourth signal line further comprises a pad layer.

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